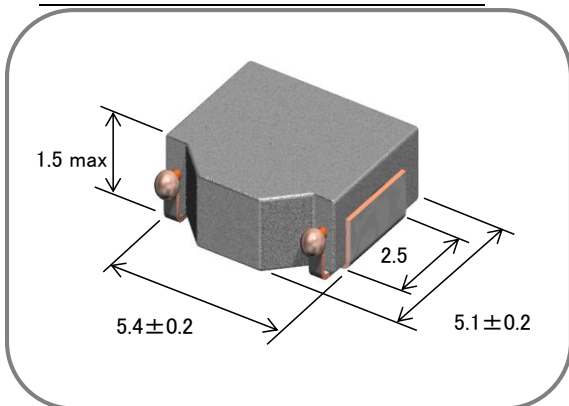


Component Image & Dimension



Features

Sampling Stage

- a) Small Footprint and Low Profile Design :
Footprint : 5.4 x 5.1 mm Typ.
Height : 1.5mm Max.
- b) High Power Handling Capability :
Small Copper Loss
Using Large Saturation Induction of Fe- based metals
- c) Flat inductance performance over temperature based on the high curie temperature of the iron powder core material.
- d) Automatic Mounting in Tape&Reel Package.

Applications

Note Book & Mobile Computer, VRM, Cellular Phone, HDD etc.

Electrical Specification

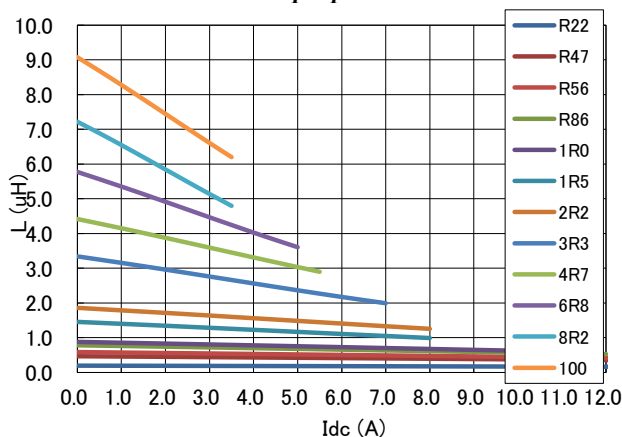
TDK Identification	Inductance		Test Freq. (kHz)	DC Resistance		Rated DC Current			
	at 0A (uH)	Tol. (%)		Spec. (m-Ohm)	Typ. (m-Ohm)	Idc 1 (A)		Idc 2 (A)	
※ SPM5015T- R22M-LR	0.22	+/-20%	100	7.0 max	6.4	16.1	21.5	10.7	11.2
SPM5015T- R47M-LR	0.47	+/-20%	100	11.0 max	10.0	14.0	14.0	9.1	9.5
※ SPM5015T- R56M-LR	0.56	+/-20%	100	12.4 max	11.3	14.0	12.5	8.0	8.4
SPM5015T- R86M-LR	0.86	+/-20%	100	21.2 max	19.3	8.3	11.0	6.5	6.8
SPM5015T- 1R0M-LR	1.00	+/-20%	100	24.8 max	22.5	7.9	8.0	6.0	6.3
※ SPM5015T- 1R5M-LR	1.50	+/-20%	100	37.1 max	33.7	5.9	7.8	4.6	4.9
SPM5015T- 2R2M-LR	2.20	+/-20%	100	43.5 max	39.5	4.3	5.7	4.6	4.8
※ SPM5015T- 3R3M-LR	3.30	+/-20%	100	84.3 max	73.3	4.2	5.6	3.2	3.4
※ SPM5015T- 4R7M-LR	4.70	+/-20%	100	101.2 max	88.0	3.5	4.7	3.1	3.2
※ SPM5015T- 6R8M-LR	6.80	+/-20%	100	128.3 max	111.6	2.9	3.9	2.6	2.7
※ SPM5015T- 8R2M-LR	8.20	+/-20%	100	170.5 max	148.3	2.4	3.2	2.2	2.3
SPM5015T- 100M-LR	10.00	+/-20%	100	170.8 max	148.5	2.5	3.3	2.4	2.5

Note. Idc 1 : Based on the inductance change. (-30% Reduction from Nominal L Value)
Idc 2 : Based on the self temperature rise. (+40 deg typ.)
Operating Temperature Range: -40 °C ~ +125 °C (including self temperature rise)

Caution: Please contact our sales person when you consider organic solvent or aqueous cleaning.

※ Simulation Data

Inductance vs. DC Superposition



Recommended pad layout

